

Firmware History

Date:	10.04.2010	Version (History):	1.2
Subject:	FW V3.51.2 for SPEED7 CPU's	Author:	Erich Heumann

Firmware V3.51.2 for SPEED7 CPUs 31x-xxxxx is valid with immediate effect and available for download at <http://www.vipa.de/en/service-support/downloads/firmware>. The firmware is compatible to its previous versions.

Further information and downloads are available at www.vipa.de.

Changes for CPUs

1. At status block the call environment can now be set under "test/call environment of block".
2. Unprogrammed SFC/SFB no longer available as empty wrapping.
3. Overflow bit of operation hours counter deleted after restarting CPU.
4. Control bits at S5 timers bit 12-15 in timer value are now correctly indicated.
5. RET-VAL at SFC/SFB adjusted to Siemens values.
6. At SFC67 X_GET and SFC68 X_PUT type CHAR are now also supported in pointer.
7. CPU 314SE can also be connected to a Master via Siemens GSD file.
8. Clear error indication at "insufficient memory capacity".
9. Easy motion also functioning via onboard counter of 314SC.
10. SFC5 WR Param and SFC54 RD-Param now also working via local data.
11. Correct operation at SFC66 X_RCV in case data block is too short.
12. Operation hours counter do not start-up after overall reset. They have to be re-started once again.
13. Timeout changed at DPV1 Slave, improved connection establishment.
14. The module address of CP343 is now taken by FC62 C_CNTRL without error.
15. In case at FB55 IP_CONFIG connection numbers higher than 64 are indicated in the parameter DB an error entry is made in the status of FB and the complete data will not be transferred.
16. The command „SAVE_PROJECT“ now requires a password in case the CPU has been password-protected.
17. In case of activated system error PU/OP interface will not issue wrong messages at start-up.
18. FM238 connected via Profibus does now start-up without error. All outputs are erased prior to start-up.
19. Wrong RET-VAL entry at use of CP341 from Siemens rectified.
20. RUN-LED is erased during initialization of backplane bus.
21. Diagnosis entry at "stop by object administration" changed to 0x4542.
22. Profinet CP from Siemens can now exchange more than 32 Byte with the CPU.
23. Wrong display of SIWAREX-U from Siemens deleted in online view of HW configuration.
24. Process image of in-/outputs can be set to all 8182 bytes via GSD file at CPU 317-4NE12.
25. MMC_LED at SAVE PROJECT via CMD file is now correctly addressed.
26. Satus block at time-/interrupt-OB does not create peripheral error.
27. Error 0xE0CB in diagnostic buffer at wrong access to SZL resp. at access on inexistant SZL.
28. Start-up problem at data exchange via Interbus rectified.

Firmware History

Changes for CP343

1. Time synchronization of SPEED7 via time server is now working without error.
2. Any-Pointer at FC6 AG-RECV of type WORD is no longer handled as BYTE.
3. Correct status indication at wrong Any-Pointer at FB14 GET resp. FB15 PUT.

Changes for Profibus

1. Transferring various consistency blocks of a module is now possible.